User Guide
EVB-ATEK160-01

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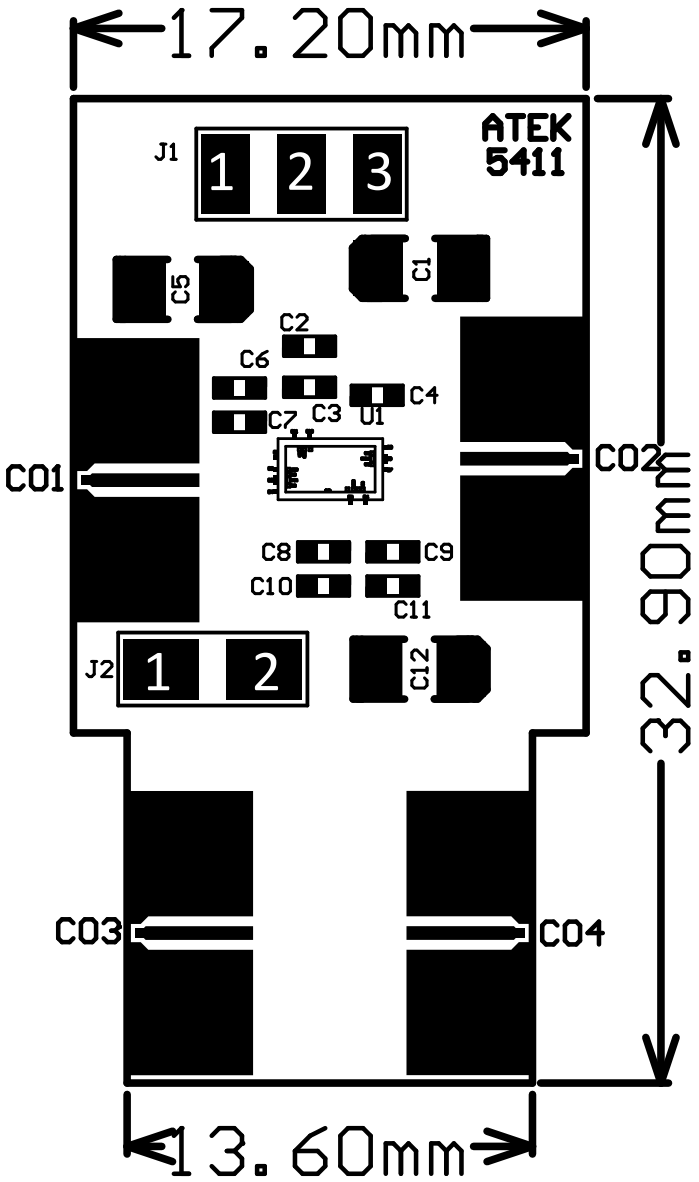
Revisions

Revision No	Revision Date	Revision Reason	Section / Page No
1.0	06.08.2024	Initial Release	

INDEX

1 GENERAL INFORMATION



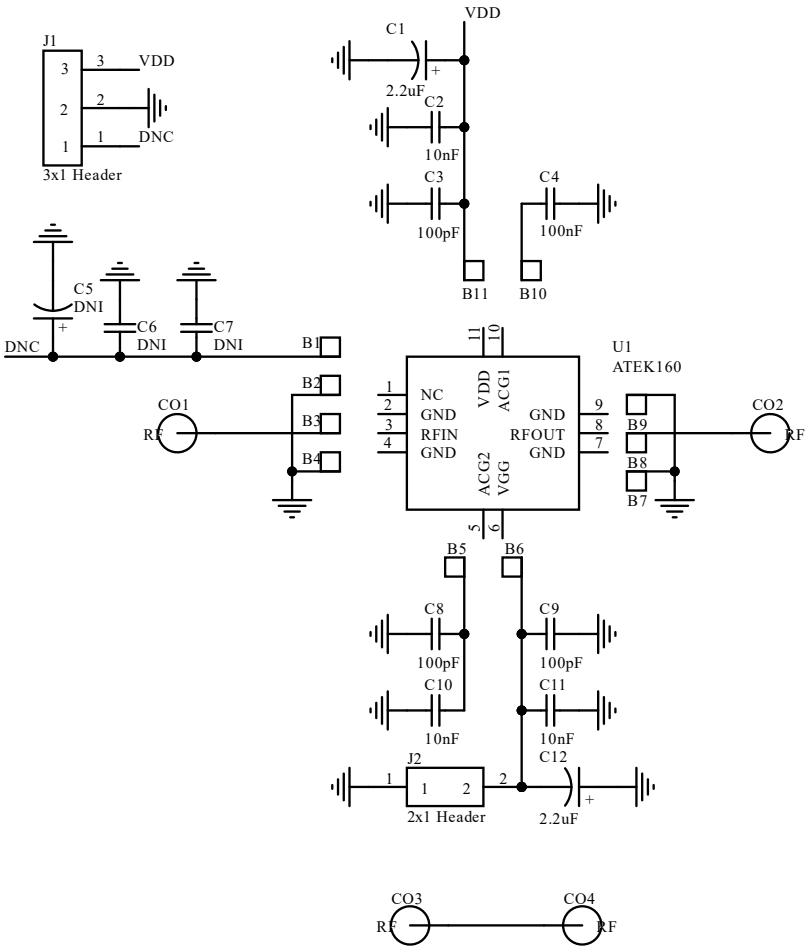
PIN Name	Definition	Comment
CO1	RF IN	K Connector
CO2	RF OUT	K Connector
CO3	RF AUX	K Connector
CO4	RF AUX	K Connector
J1.1	Do Not Connect	2.54mm Header
J1.2, J2.1	GND	2.54mm Header
J1.3	VDD	2.54mm Header
J2.2	VGG	2.54mm Header

Notes:

1. Refer to the datasheet for VDD and VGG Voltages details.

2 DESIGN INFORMATION

2.1 SCHEMATIC



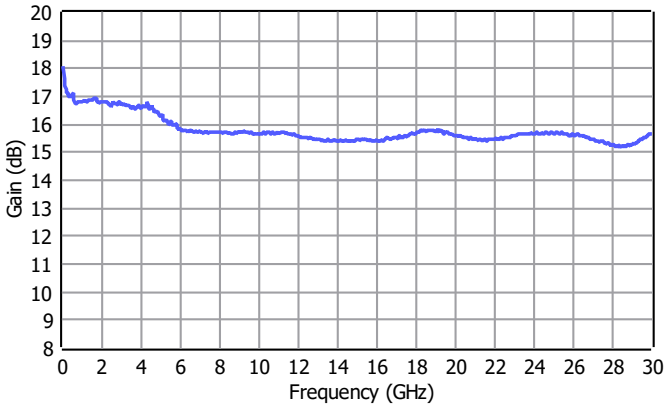
2.2 BOM

Designator	Footprint	Qty	Comment	PN
C1, C12	CASEA	3	2.2uF	
C2, C10, C11	0402	4	10nF	
C3, C8, C9	0402	4	100pF	
C4	0402	1	100nF	
C5	CASEA	1	DNP	
C6, C7	0402	2	DNP	
CO1, CO2, CO3, CO4	K Connector	4	K Connector	ATEK9292
J1	3x1 Header	1	3x1 Header	
J2	2x1 Header	1	2x1 Header	
U1	DIE	1	Low Noise Amplifier	ATEK160

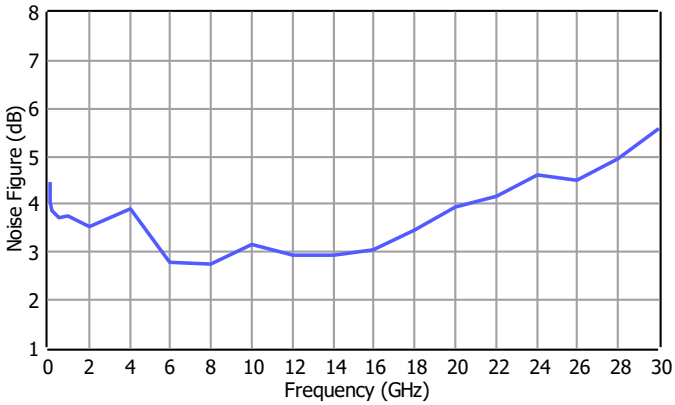
3 TYPICAL PERFORMANCE PLOTS

Conditions unless otherwise specified: V_{DD} = 8 V, Idq = 60mA. V T = 25 C, CW. For details, please refer to the datasheet.

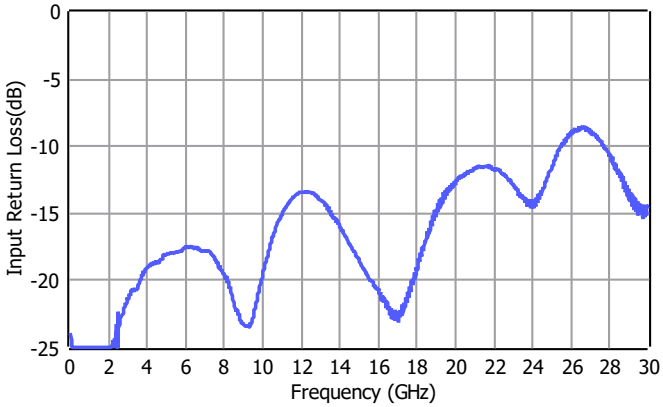
Gain



Noise Figure



Input Return Loss



Output Return Loss